



**DIPLOMA COURSE IN ELECTRONICS & COMMUNICATION ENGINEERING  
SECOND SEMESTER  
Scheme of Study and Examination**

SL. No.	Q P CODE	Subject	Contact Hours			Exam Duration	Scheme of Examination			Total Max Marks	Min. Marks for passing
			Theory	Pract.	Total		End Exam		I.A. Marks		
							Max.Marks	Min. Marks			
1	14DEC21	Applied Mathematics-II	4	---	4	3	100	35	25	125	45
2	14DEC22	Semiconductor Devices	4	---	4	3	100	35	25	125	45
3	14DEC23	Digital Electronics	4	---	4	3	100	35	25	125	45
4	14DEC24	English Communication- II	2	---	2	3	100	35	25	125	45
<b>PRACTICALS</b>											
5	14DEC25P	Semiconductor Devices Lab	---	4	4	3	100	50	25	125	60
6	14DEC26P	Digital Electronic Lab	---	4	4	3	100	50	25	125	60
7	14DEC27P	Programming with C Lab	---	4	4	3	100	50	25	125	60
8	14DEC28P	English Communication-II Lab	---	2	2	3	100	50	25	125	60
<b>Total</b>			<b>14</b>	<b>14</b>	<b>28</b>	<b>24</b>	<b>800</b>	<b>340</b>	<b>200</b>	<b>1000</b>	<b>420</b>