

SCHOOL OF ENGINEERING & APPLIED SCIENCES
DIPLOMA COURSE IN ELECTRONICS AND COMMUNICATION ENGINEERING

THIRD SEMESTER

Scheme of Study and Examination

(With effect from 2014-2015)

Sl. No.	Subjects	Q P CODE	Contact Hours			Exam. Duration	Scheme of Examination			Total Max. Marks	Min Marks for passing (with IA)
			Theory	Pract.	Total		End Exam.		I. A. Marks		
							Max. Marks	Min. passing Marks			
Theory											
1	Analog Electronic Circuits	14DEC31	4		4	3	100	35	25	125	45
2	Digital Electronics & Introduction to Microprocessors	14DEC32	4		4	3	100	35	25	125	45
3	Analog Communication	14DEC33	4		4	3	100	35	25	125	45
4	Electronic Measurements and Instrumentation	14DEC34	4		4	3	100	35	25	125	45
Practicals											
5	Analog Electronics Circuits lab	14DEC35P		4	4	3	100	50	25	125	60
6	Analog Communication Lab	14DEC36P		4	4	3	100	50	25	125	60
7	PCB Design & Simulation Lab	14DEC37P		4	4	3	100	50	25	125	60
8	Communication & Life skill – I	14DEC38P		3	3	3	50	25	75	125	60
		Total	16	15	31	24	750	315	250	1000	420